

This diagram shows the top plan view of a semiconductor device 2. The device has a central square region 10, which is a well or channel region. This central region is surrounded by a rectangular frame 14. The frame 14 is composed of a top portion 18 and a bottom portion 16, which are connected by side portions 20. The top portion 18 has a semi-circular notch 14 at its center. The bottom portion 16 has a semi-circular protrusion 20 at its center. Four horizontal bars 4, 5, 6, and 7 extend from the central region 10 to the outer edges of the device. Each bar has a corresponding label on the left (4A, 5A, 6A, 7A) and right (4, 5, 6, 7) sides. The bars are connected to the central region 10 by small rectangular pads 22, 24, 25, and 26. The entire device is labeled 2 with an arrow pointing to it.

FIG. I

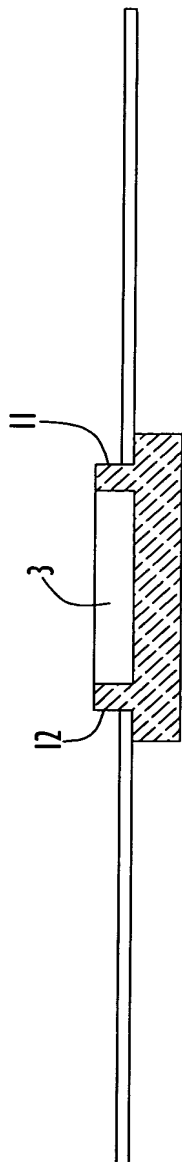


FIG. 2

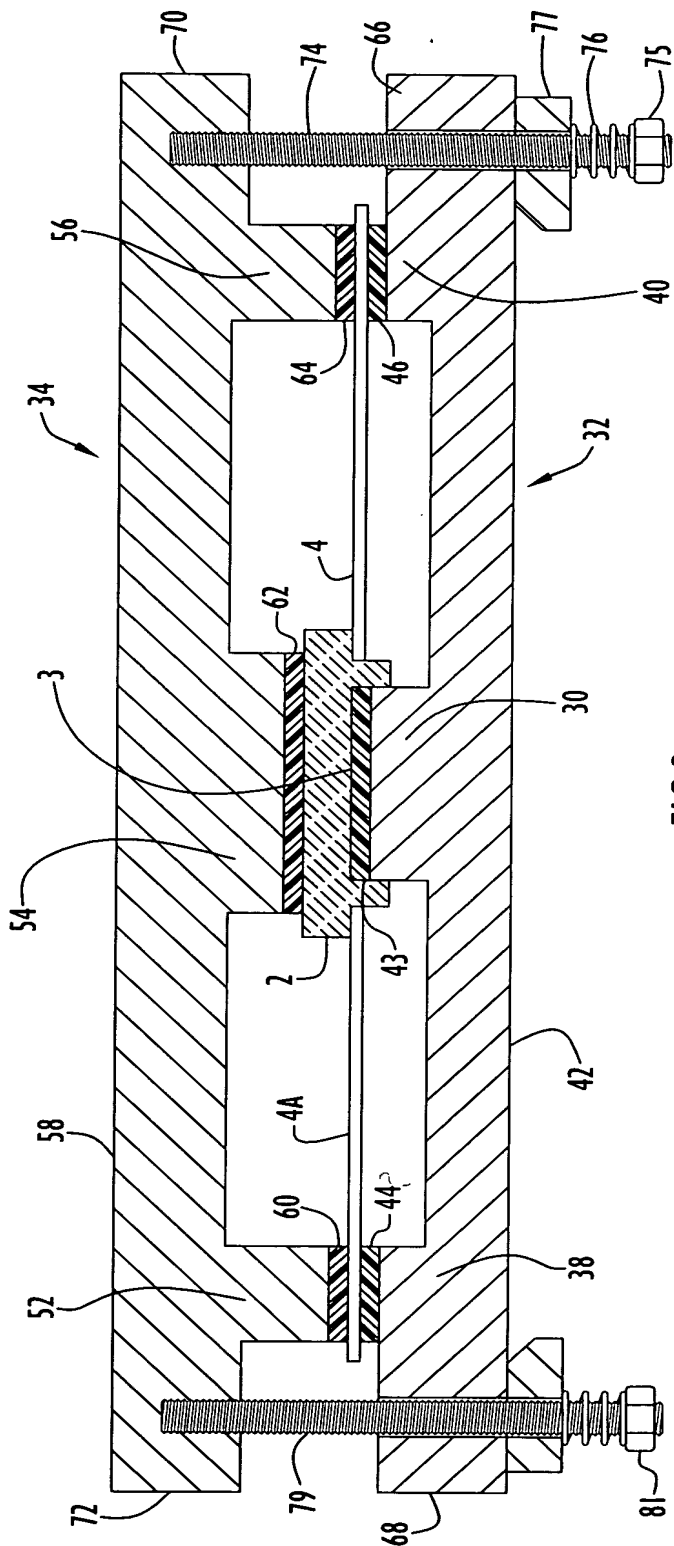
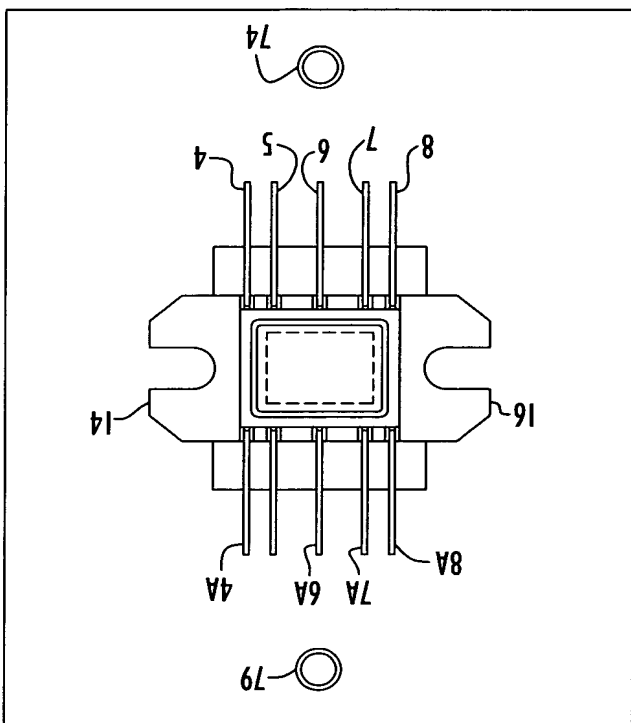


FIG. 3

FIG. 4



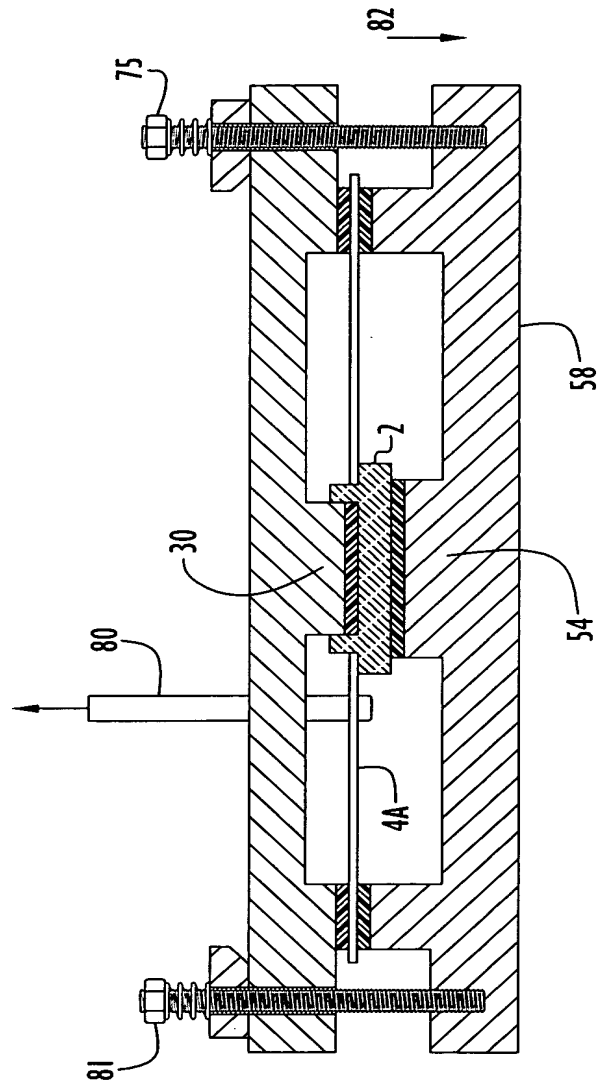


FIG. 5

FIG. 6

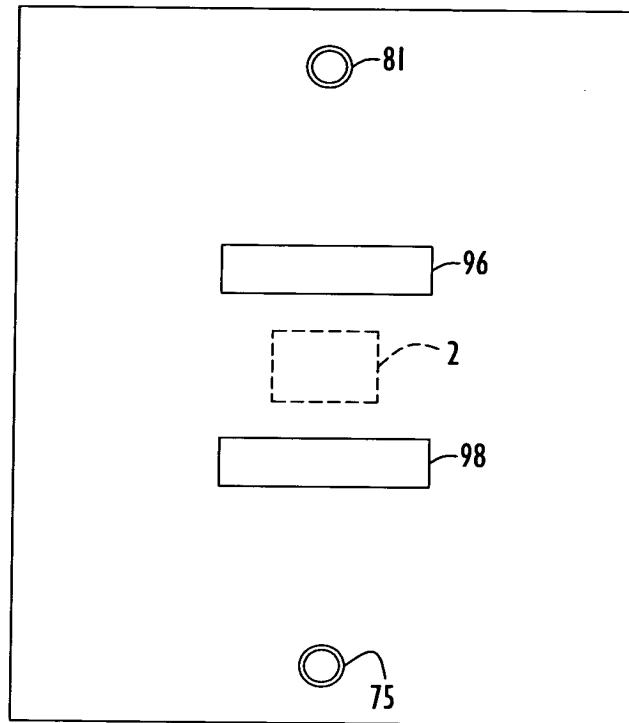


FIG.6

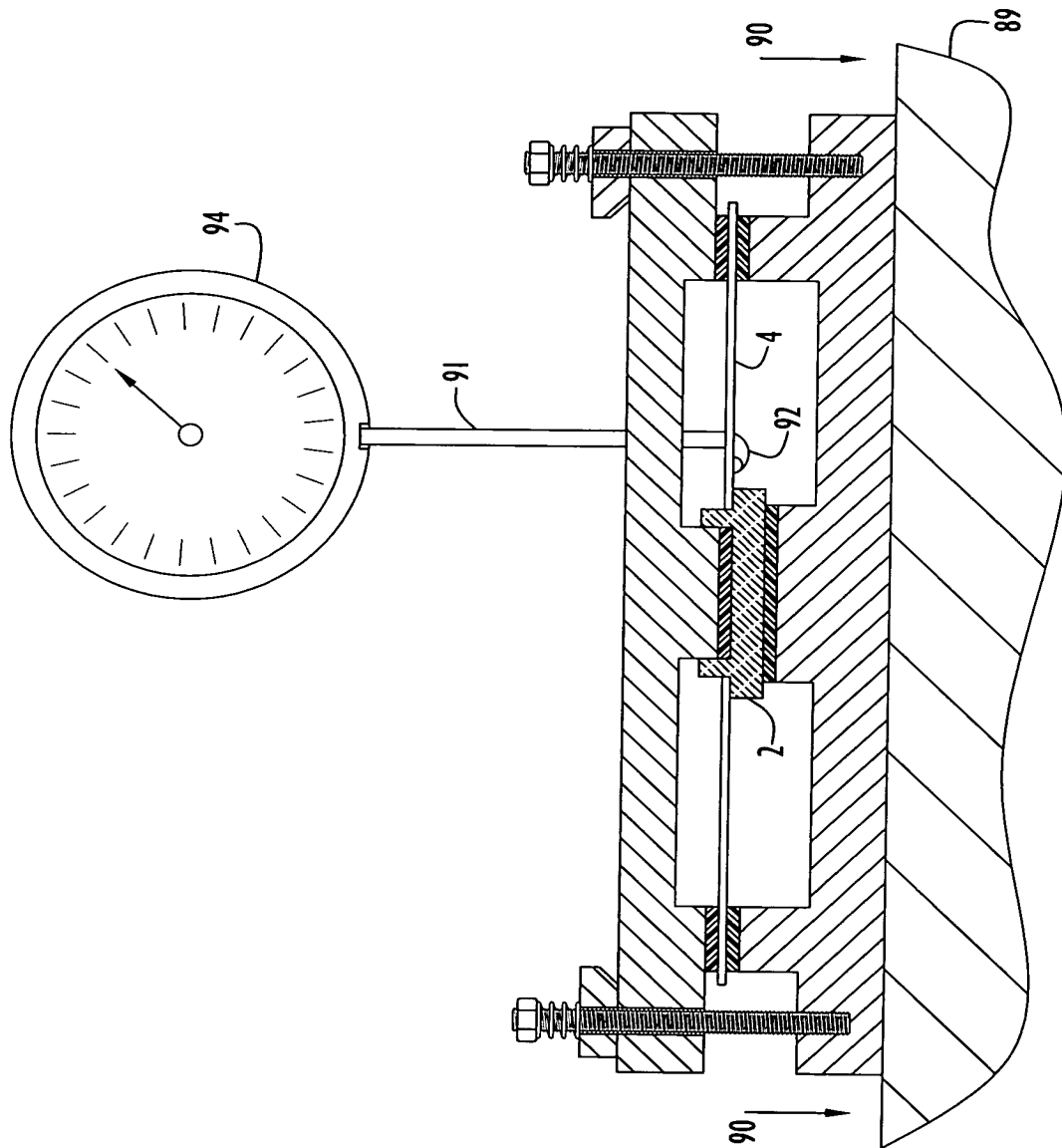


FIG. 7